Our problem was to implement hypothetical CPU. To do this we also implemented an assembler and an execution simulator. Before starting to project, we tried to understand how the CPU works. We executed the example given in the description by hand and observed the mechanism behind. We studied on addressing modes and operation encoding.

First of all, we created dictionaries for instructions and registers matching with their codes. Then, we read the file to detect labels. We put them into another dictionary with their addresses which are calculated during reading process. After that, we read the file line by line once more. We tokenized lines and decided their operation codes according to instruction, their addressing modes according to operand. If the operand is a character we took ascii value of it as an operand. If the operand is enclosed by “[]” we treated inside as an address and decide its address accordingly. If the inside of the parentheses is a register, we took its value from register dictionary as the operand. If not, we took it directly. We converted the hex

operation code, addressing mode, operand to binary, formatted them and then we concatenated them all and converted hex again. This way, we created instruction codes and wrote them into .bin file.

At the second part of the project, firstly we created a list whose length is 65536 referring to our 64KB memory. Then, we created a dictionary for the registers and their contents. Also we initialized sign, zero and carry flags as false. After that, we divide our hex instruction codes to three parts so that they can fit into memory, each instruction occupied three index since an instruction is 24 bits. To execute instructions we got from the memory, we constructed? a while loop. In this while loop, we got three consecutive values from the memory and we decided their operation codes, address modes and operand of each instruction. Then, for each operation code, we formed an if statement to do necessary operations. After we decided our operation, inside these if statements we checked the addressing mode of it. According to address mode either we took operand directly as immediate data or we treated it as an address and got the data from the memory. In addition, if the operand is a register we took the data from register dictionary. We had some functions in order to avoid repetition. For example, “getdatafrommemo” function takes the address as the parameter and returns the data from the memory.